

規格書


SPECIFICATION

品名
STYLE NAME : SWITCHING POWER SUPPLY

型號
MODEL NO. : DZRH-2800V2

料號
PART NO. :

版次
REVISION : A2

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Revision

Rev.	Page	Item	Date	Description
A2	9	3.7	JAN-08-2019	UPDATE 3.7 Remote on/off control: 1. Logic level low (power supply ON) 2. Logic level high (power supply OFF)



MODEL NO. DZRH-2800V2

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1.0 Scope

This specification defines the performance characteristics of a grounded, Dc input 800 watts, 2 output level power supply. This specification also defines world wide safety requirements and manufactures process test requirements.

2.0 Input requirements

2.1 DC Input Voltage, Frequency and Current (Rating: -36~-72Vdc, Normal: -48Vdc)

2.2 DC Inrush Current

25 amps max. @ -48VDC Input
(at 25 degrees ambient cold start for each power unit)
High-Frequency peak amplitudes lasting less than 50uS
(i.e.those caused by EMI capacitors)shall be ignored.

2.3 Steady-state current

-36 ~ -72 VDC / 30 ~15 amp (21 amp at -48VDC).

2.4 Efficiency

The maximum power supply system efficiency shall be 87%, measured at nominal input voltage -48Vdc and 50% loading.

NOTE:

The different harness conditions and/or the accuracy of measurement instruments affect the test result of output voltage and efficiency. Harness conditions are such as cable length, wire gauge, the connector types, total harness amounts.

3.0 Output requirements

3.1 Output Current / Loading

The Table 2 define current rating. The power supply shall meet both static and dynamic voltage regulation requirements for minimum load condition.

Output	+12V	+12VSB
Max. Load	66A	2.1 A
Min. Load	0A	0A

Table 2 – Output Current:

Note:

1. Total power:800W (MAX)
2. DC36~40V input: Once AC or PS_ON turn on power supply, the total max. power is requested to be less than 80% of MAX. LOAD.

3.2 DC Voltage Regulation

The power supply output voltages must stay within the following voltage limits when operating at steady state and dynamic loading conditions. All outputs are measured with reference to the return remote sense (ReturnS) signal. The +12V and +12VSB outputs are measure at the power supply connectors' references to ReturnS.

Output Voltage	+12V	+12VSB
Load Reg.	+5/-5%	+5/-5%
Line Reg.	±1%	±1%

Table 3 – Regulation

3.3 Ripple and noise

The maximum allowed ripple/noise output of the power supply is defined in Table 4.

This is measured over a bandwidth of 0 Hz to 20 MHz at the power supply output connectors. A 100 μ F tantalum capacitor in parallel with a 0.1 μ F ceramic capacitor are placed at the point of measurement.

Output Volatage	Ripple	Ripple+Noise
+12V	120mV(P-P)	120mV(P-P)
+12VSB	120mV(P-P)	120mV(P-P)

Table 4 - Ripple and noise

Note : For Ripple Noise requirements +12V min Loading is 2A.

3.4 Dynamic Loading

The output voltages shall remain within the limits specified in Table 5 for the step loading and within the limits specified in Table 6 for the capacitive loading. The load transient repetition rate shall be tested between 50Hz and 5kHz at duty cycle ranging from 10%-90%. The load transient repetition rate is only a test specification. The Δ step load may occur anywhere within the MIN load to the MAX load range.

Output	Δ Step Load Size	Load Slew Rate	Capacitive Load
12V	65% of max load	0.5 A/ μ s	2200 μ F
+12VSB	25% of max load	0.5 A/ μ s	1 μ F

Table 5 – Transient Load Requirements

Note: For dynamic voltage regulation requirements +12V min Loading is 2A.

3.5 Capacitive Loading

The power supply shall be stable and meet all requirements with the Table 6 capacitive loading ranges.

Output	MIN	MAX	Units
+12 V	10	11,000	μF
+12 VSB	1	350	μF

Table 6 – Capacitive Loading Conditions

3.6 Timing Requirements

These are the timing requirements for the power assembly operation. The output voltages must rise from 10% to within regulation limits (T_{vout_rise}) within 0 to 20mS. The +12V output voltages should start to rise at about the same time. All outputs must rise monotonically. Each output voltage shall reach regulation within 50 ms (T_{vout_on}) of each other during turn on of the power supply. Each output voltage shall fall out of regulation within 400 ms (T_{vout_off}) of each other during turn off. Figure 1 and figure 2 show the turn On and turn Off timing requirement. In Figure 2, the timing is shown with both DC and PSON# controlling the On/Off of the power supply.

Item	Description	MIN	MAX	Units
Tvout_rise	Output voltage rise time from each main output.		20	ms
Tsb_rise	Ouput voltage rise time for the 12VSB output.		25	ms
Tvout_on	All main outputs must be within regulation of each other within this time.		50	ms
Tvout_off	All main outputs must leave regulation within this time.		400	ms

Table 7 – Output Voltage Timing

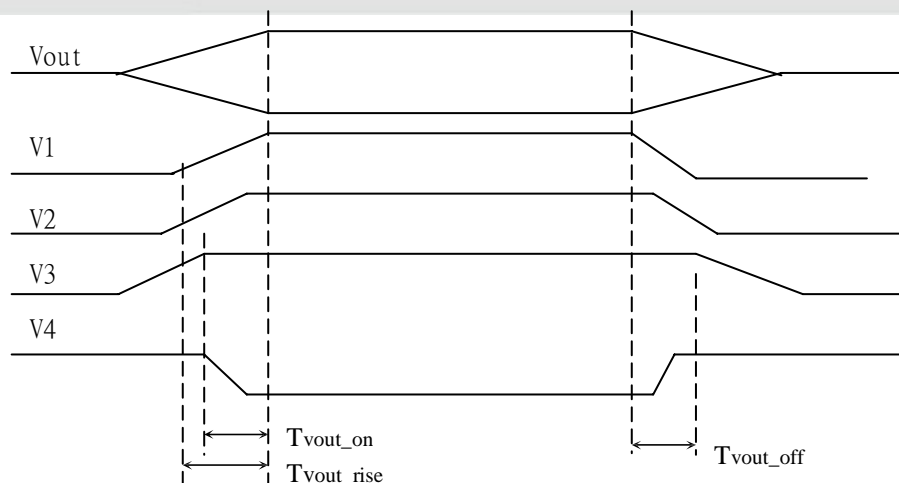


Figure 1: Output Voltage Timing

Item	Description	MIN	MAX	UNITS
Tsb_on_delay *1	Delay from DC being applied to 12VSB being within regulation.		1500	ms
Tac_on_delay *1	Delay from DC being applied to all output voltages being within regulation.		2500	ms
Tsb_vout *1	Delay from 12VSB being in regulation to O/Ps being in regulation at DC turn on.	2	1000	ms
Tpwok_on *1	Delay from output voltages within regulation limits to PWOK asserted at turn on.	100	500	ms
Tvout_holdup	Time all output voltages stay within regulation after loss of DC.	1		ms
Tpwok_off	Delay from PWOK deasserted to output voltages (12V) dropping out of regulation limits.	1		ms
Tsb_holdup	Time 12VSB output voltage stays within regulation after loss of DC.	2		ms
Tpson_on_delay *1	Delay from PSON# active to output voltages within regulation limits.	5	400	ms
Tpson_pwok *1	Delay from PSON# deactive to PWOK being deasserted.		50	ms

Table 8 – Turn On/Off Timing

*1. DC36~40V input: Once DC or PS_ON turn on power supply, the total max. power is requested to be less than 80% of MAX. LOAD.

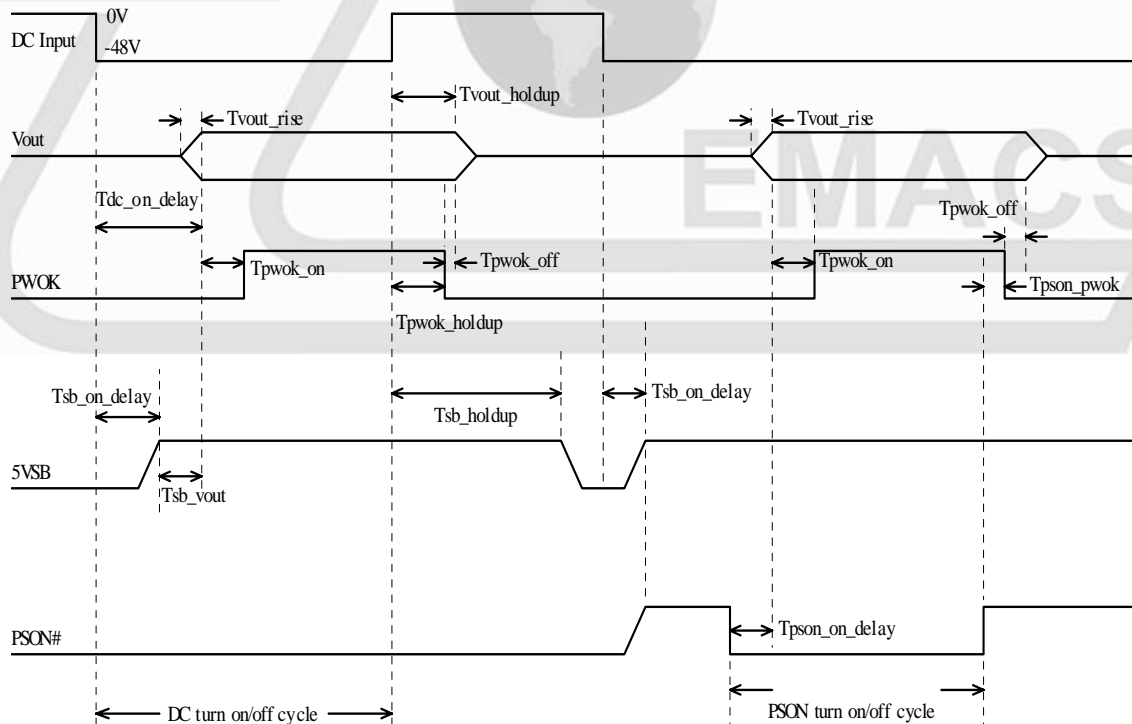


Figure 2: Turn On/Off Timing(Power Supply)

3.7 Remote on/off control

The PSON# signal is required to remotely turn on/off the power supply. PSON# is an active low signal that turns on the +12V power rails. When this signal is not pulled low by the system, or left open, the outputs (except the +12VSB) turn off. This signal is pulled to a standby voltage by a pull-up resistor internal to the power supply. Refer to Figure 2 for timing diagram.

Signal Type	Accepts an open collector/drain input from the system. Pull-up to VSB located in power supply.	
PSON# = Low	ON	
PSON# = Open or High	OFF	
	MIN	MAX
Logic level low (power supply ON)	0 V	0.3 V
Logic level high (power supply OFF)	2.0 V	3.46 V
Source current, Vpson = low		4 mA
Power up delay: Tpson_on_delay	5 ms	400 ms
PWOK delay: Tpson_pwok		50 ms

Table 9 – PSON# Signal Characteristic

3.8 Overshoot at Turn-on / Turn-off

Any output overshoot at turn on shall be less than 10% of the nominal output value. Any overshoot shall recover to within regulation in less than 10ms.

3.9 standby outputs

The 12VSB output shall be present when an DC input greater than the power supply turn on voltage is applied.

4.0 Protection

Protection circuits inside the power supply shall cause only the power supply's main outputs to shutdown. If the power supply latches off due to a protection circuit tripping, either a DC OFF for 15 sec, or PSON# cycle HIGH for 1 sec must be able to restart the power supply.

4.1 Over power protection

The power supply shall provide over power protection on the power supply latches all DC output into a shutdown state. Over power of this type shall cause no damage to power supply, after over load is removed and a power on/off cycle is initiated, the power supply will restart.

Trigger point total power min. 110%, max. 150%.

4.2 Over voltage protection

The power supply shall shut down in a latch off mode when the output voltage exceeds the over voltage limit shown in Table 10.

Voltage	Minimum	Maximum	Shutdown
+12V	+12.8V	+15.0V	Latch Off

Table 10 -Over Voltage Protection

4.3 Over current protection

This power supply shall have current limit to prevent the +12V outputs from exceeding the values shown in Table 11. The current limit shall not trip under maximum continuous load or peak loading as described in Table 5. The power supply shall latch off if the current exceeds the limit. The latch shall be cleared by toggling the PSON# signal or by cycling the AC power. The power supply shall not be damaged from repeated power cycling in this condition. The +12VSB output shall be shorted circuit protected so that no damage can occur to the power supply.

Voltage	Minimum	Maximum	Shutdown
+12V	110%	150%	Latch Off

Table 11 – Over Current Protection

4.4 Short Circuit Protection

4.4.1: A short circuit placed on any DC output to DC return shall cause no damage.

4.4.2: The power supply shall be latched in case the DC return short circuit is taken place at +12V.

4.4.3: The power supply shall be auto-recovered in case any short circuit is taken place at +12VSB.

5.0 Environmental Requirements

5.1 Temperature

Operating Temperature Range:	0°C ~ 50°C
Non-Operating Temperature Range:	-20°C ~ 80°C

5.2 Humidity

Operating Humidity Range:	20% ~ 80%RH non-condensing
Non-Operating Humidity Range:	10% ~ 90%RH non-condensing

5.3 Altitude

Operating Altitude Range:	Sea level to 16,000 ft
Non-Operating Altitude Range:	Sea level to 40,000 ft

6.0 Safety

The power supply must be certified to the safety standard listed following:

6.1 Underwriters laboratory (UL).

The power supply designed to meet UL 60950 & UL 62368-1.

6.2 Canadian standards association (CUL)

The power supply designed to meet CSA C22.2 No. 60950 & No. 62368-1.

6.3 TUV

The power supply shall be designed to meet TUV EN-60950 & EN-62368-1.

6.4 CCC Standards

The power supply shall be designed to meet GB/T9254-2008, GB4943.1-2011, GB17625.1-2012.

6.5 Power Line Transient.

The power supply shall be designed to meet the following standards

a.) EN 61000-4-2(ESD) Criterion B, $\pm 4KV$ by contact, $\pm 8KV$ by air.

b.) EN 61000-4-4(EFT) Criterion B, $\pm 1KV$.

c.) EN 61000-4-5(SURGE) Criterion B, Line-Line $\pm 1KV$
Line-Earth $\pm 2KV$.

6.6 RFI / EMI Standards

The power supply shall comply with the following radiated and conducted Emissions standards,

a.) FCC part 15. class A.

b.) CISPR 22 (EN 55032). class A.

6.7 Production Line Testing

100% of the power supply production must have the following test performed.

Each power shall be marked indicating the testing was done and passed. Typically this is done by stamping or labeling the power supply with "Hi-pot test OK".

6.8 Hi-Pot Testing

Primary to secondary : 2000 VAC for 60 sec.

Primary to FG : 1500 VAC for 60 sec.

For production purpose:

Primary to FG :1500VAC for 1 sec.

6.9 Insulation resistance

Primary to secondary : 20 meg. Ohm min. 500 VDC

Primary to FG : 20 meg. Ohm min. 500VDC

7.0 Reliability

7.1 Burn in

All products shipped to customer must be burn in. The burn in shall be performed at high line voltage.

7.2 MTBF

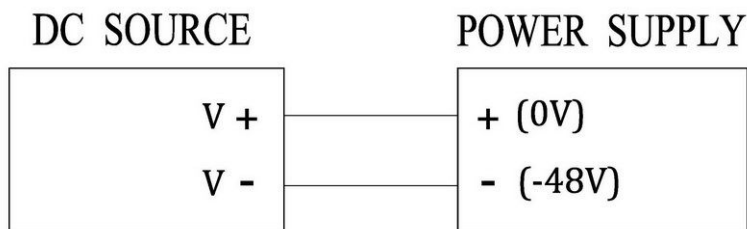
The MTBF of the power supply shall be calculated utilizing the Part-Count Analysis method of MIL-HDBK-217F.

The calculated MTBF of the power supply is 173,645 hours at ambient temperature 25 degree.anical requirements

8.0 Mechanical requirements

Physical dimension : 185mm (D) x 73.5mm (W) x 40mm (H)

9.0 Instruction of DC source input cable connection



10.0 Output power derating characteristics

10.1 Total max. power V.S Input Voltage

